

2003
6247
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inputting at least a first portion of said selected one message to a control processor;
selecting control information in said inputted first portion of said selected one message and communicating said selected control information to a plurality of dedicated register memories;
controlling a digital switch on the basis of a plurality of comparisons at said memories;
outputting selected other portions of said message stream to said plurality of processors;
processing said selected other portions of said message stream simultaneously; and
controlling the reception or presentation of television programming in accordance with said selected other portions.

110. (New Claim) The method of claim 109, further comprising the step of programming said control processor to execute a controlled function in response to an execution instruction contained in said one message.

111. (New Claim) The method of claim 110, further comprising the step of programming said control processor to compare information stored at some or all of a first of said plurality of dedicated register memories with control function invoking information.

2003
6257

112. (New Claim) The method of claim 111, further comprising the step of programming said control processor to compare information stored at some or

all of a second of said plurality of dedicated register memories with information that identifies the composition of said one message.

113. (New Claim) The method of claim 109, wherein said plurality of dedicated register memories include an input signal register memory and said step of selecting control information in said inputted first portion of said selected one message and communicating said selected control information to a plurality of dedicated registers memories comprises:

communicating said at least a first portion of said selected one message to said input signal register memory;

selecting information at said input signal memory to compare or communicate; and

communicating said control information to a second of said plurality of dedicated register memories.

114. (New Claim) The method of claim 109, further comprising the step of communicating at least one of said other portions of said message to said input signal register memory.

115. (New Claim) The method of claim 114, further comprising the step of communicating said at least one of said other portions of said message from said input signal register memory to one of said digital switch and a second of said plurality of processors.

116. (New Claim) The method of claim 109, wherein said control processor receives said at least a first portion of said message from a first of said

plurality of processors, said method further comprising the step of controlling said digital switch to communicate one or more other portions of said message to a second of said plurality of processors.

117. (New Claim) The method of claim 116, wherein said first processor performs one of (1) converting information detected in said message stream based on protocols and (2) assembling processor code based on information detected in said message stream, said method further comprising the step of communicating machine language code to said second processor in said one or more other portions of said message.

118. (New Claim) The method of claim 109, wherein said control processor controls a decryptor to decrypt at least some of said message, said method further comprising the step of controlling said digital switch to communicate said at least some of said message to or from said decryptor.

119. (New Claim) The method of claim 118, further comprising the steps of:

storing a decrypted portion of said at least some of said message at some of said plurality of dedicated register memories; and
processing decrypted portions of said message stream simultaneously.

120. (New Claim) The method of claim 109, wherein a standard identifies a signal to be processed, said method further comprising the step of identifying the start of said selected one message based on said standard.

121. (New Claim) The method of claim 120, further comprising the step of:

programming said television receiver to compare the contents of at least a first of said plurality of dedicated register memories to a second of said plurality of dedicated register memories; and

storing said standard at one of said plurality of dedicated register memories.

122. (New Claim) The method of claim 120, wherein said receiver identifies, based on said standard, one of (1) an end of a prior message and (2) a header in said selected one message.

123. (New Claim) The method of claim 120, further comprising the step of causing said control processor to process an interrupt signal based on said identified signal to be processed.

124. (New Claim) The method of claim 109, wherein said television receiver includes a video monitor and information contained in said message stream controls a first of said plurality of processors to generate a video signal to be displayed as part of said television programming, said method further comprising the step of causing one of said control processor and said digital switch to communicate to said first processor a first signal which causes said first processor to communicate said video signal to said video monitor.

125. (New Claim) The method of claim 124, wherein said television receiver includes a speaker and information contained in said message stream

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controls a second of said plurality of processors to generate an audio signal containing audio to be emitted as part of said television programming, said method further comprising the step of causing one of said control processor and said digital switch to communicate to said second processor a second signal which causes said second processor to communicate said audio signal to said speaker.

126. (New Claim) The method of claim 124, wherein said first of said plurality of processors generates said video signal in accordance with at least one of said selected other portions of said message stream, said method further comprising the step of controlling said digital switch to communicate said at least one of said selected other portions of said message stream to said first of said plurality of processors.

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127. (New Claim) The method of claim 109, wherein said at least one of said other portions of said message stream contains first processor code addressed to a processor that generates information content of a video or audio signal, said method further comprising the steps of:

inputting to said control processor second processor code received in said selected one message; and

communicating, in accordance with said second processor code, said at least one of said other portions of said message stream to said processor that generates said information content.

128. (New Claim) The method of claim 127, wherein said second processor code programs said control processor to select control information in

said message stream and communicate said selected control information to said plurality of dedicated register memories, said method further comprising the step of processing control information of a new composition and/or length in accordance with said second processor code.

129. (New Claim) A method of video signal processing at a video receiver, said video receiver having a plurality of processors, said method comprising the steps of:

- receiving an information transmission containing a digital video signal and a message stream;
- detecting said message stream in said information transmission;
- selecting one message of said detected message stream;
- inputting at least a portion of said selected one message to a control processor;
- selecting control information in said inputted portion of said selected one message and communicating said selected control information to at least one dedicated register memory;
- controlling a digital switch on the basis of a plurality of comparisons at said at least one dedicated register memory;
- outputting selected portions of said message to said plurality of processors;
- processing said selected portions of said message simultaneously; and
- controlling the reception or presentation of video in accordance with said one message.

130. (New Claim) The method of claim 129, wherein said information transmission is processed using forward error correction techniques.

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131. (New Claim) A method for an origination station or intermediate transmission station to control television or video signal processing at a television or video receiver, said television or video receiver having a plurality of processors, said method comprising the steps of:

- (1) receiving an information transmission, said information transmission containing a message stream;
- (2) receiving a control signal which operates at a transmitter station to communicate said information transmission to a transmitter; and
- (3) transmitting said message stream, said message stream to effect said receiver station to select a portion of one message from said message stream and control the reception or presentation of television programming or video information in accordance with said one message.

132. (New Claim) The method of claim 131, wherein said step of receiving an information transmission is at a signal generator operatively connected to said transmitter, and further comprising the steps of:

generating first cadence information which is effective at said receiver station to execute a predetermined instruction and at least one message element containing one or more instructions to be directed to a specific one of said plurality of processors; and

embedding said cadence information and said at least one message element in said information transmission before communicating said information transmission to said transmitter.

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133. (New Claim) The method of claim 132, wherein said specific processor includes a plurality of dedicated register memories, said method further comprising the step of:

communicating to said signal generator second cadence information which operates at said plurality of dedicated register memories to select said portion of one message.

134. (New Claim) A method for an origination station or intermediate transmission station to control processing of signals in a television or video receiver, said television or video receiver having a plurality of processors, said method comprising the steps of:

- (1) receiving an information transmission, containing a message stream; and
- (2) causing a portion of said message stream to be communicated to a transmitter at a specific time, thereby to transmit said portion of said message stream, said portion of said message stream to effect said receiver station to control the reception or presentation of some television programming or video information in accordance with said message portion.

135. (New Claim) The method of claim 134, wherein said television or video receiver includes a processor and a digital switch, said method further comprising the steps of:

generating (1) cadence information which is effective at said receiver station to execute a predetermined instruction and (2) at least one message element containing one or more instructions to be directed to said processor;